Customer Success Story

Implementing Advanced Formal Verification Methodology

The Customer Challenge

Valens, a leading provider of HDBaseT connectivity solutions, needed to enhance their verification flow to improve design quality and productivity. The challenge was to introduce Formal Verification methodology into their existing processes while maintaining ongoing development activities. This required implementing new verification approaches across multiple critical components, including their core HDBaseT packet interface protocol, Custom RISC microcontroller, and multi-port Queue Manager.

The Veriest Solution

Veriest Solutions provided comprehensive formal verification expertise:

- Defined and implemented new Formal Verification methodology
- Created Formal VIP for HDBaseT packet interface protocol
- Extended verification coverage for Custom RISC microcontroller
- Implemented formal methods for multi-port Queue Manager
- Integrated formal verification into existing design flows
- Enabled knowledge transfer for methodology adoption

Result

The partnership delivered outstanding outcomes:

- Successfully integrated Formal Verification into existing flows
- Achieved higher verification coverage with reduced resources
- Improved overall design quality and productivity
- Minimized disruption to ongoing development activities
- Established foundation for continued formal verification usage
- Created verification methodology showcased at CDNLive Israel

> "We were extremely satisfied with the services we received from Veriest, whose expertise was critical in our efforts to introduce Formal Verification. We were able to quickly add this tool to our current methodology and achieve great results – with minimum disruption to our daily activities."
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> **Itamar Nitzan, Verification Manager at Valens**