Veriest **nuvoTon**

Customer Success Story

Verification of Computing MCU Devices

The Customer Challenge

Nuvoton needed to verify critical functionality in their family of high-end computing microcontroller SoC devices used for building reliable, secure and resilient computing platforms. The project required implementing complex verification environments using multiple languages and methodologies while meeting tight schedules. This demanded seamless integration of Specman and System-Verilog components using UVM Multi-Language methodology.

The Veriest Solution

Veriest Solutions provided comprehensive verification expertise:

- Defined and implemented verification plans for complex SoC
- Created multi-language verification environments using UVM-ML
- Integrated legacy code with third-party verification components
- Executed complex verification tasks for design quality assurance
- Maintained close collaboration with Nuvoton's team
- Adapted resources flexibly to meet dynamic project needs

Result

The partnership delivered outstanding outcomes:

- Successfully verified critical MCU functionality
- Achieved seamless integration of multi-language components
- Improved verification productivity through UVM-ML methodology
- Completed verification under tight schedule constraints
- Established foundation for reliable computing platforms
- Delivered high-quality verification for complex SoC design

> *"Right from the very first moment on board, with their vast experience, Veriest engineers brought substantial contribution to our critical and challenging project, taking on the execution of some of the more complex verification tasks required to assure the quality of our design. All that, in the most professional and dedicated manner I could expect."*
> **Ilan Margalit, Director of IC Development at Nuvoton**