Customer Success Story

Pioneering AI Chip Development

The Customer Challenge

NeuReality, an innovative AI technology company, faced the complex task of developing the world's first Network Addressable Processing Unit (NAPU) for AI inference solutions. They needed to create a groundbreaking chip that could revolutionize data centers running large AI applications, from generative AI to cybersecurity. The project required exceptional expertise in implementing sophisticated system-on-chip (SoC) designs on TSMC N7 technology, a cutting-edge manufacturing process.

The Veriest Solution

Veriest Solutions deployed their expert engineering teams to:

- Lead the physical implementation of the NR1 chip, the world's first NAPU
- Provide critical expertise in implementing complex SoC designs
- Ensure the highest quality, performance, and reliability standards were met
- Support the transition from final validated design to TSMC manufacturing
- Contribute essential functionality for NeuReality's AI-based solutions

Result

The collaboration led to several significant achievements:

- Successful development and validation of the revolutionary NR1 chip
- Enhanced performance and cost efficiency for AI deployment in data centers
- Successful transition to manufacturing phase with TSMC
- Establishment of a foundation for heterogeneous, decomposable AI-centric data centers

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"The Veriest team demonstrated exceptional proficiency in implementing system-on-chip designs tailored for NeuReality's custom AI infrastructure. Veriest has proven to be an invaluable partner, showcasing exceptional skills and unwavering dedication to our AI-centric NR1 chip that recently moved from final, validated design to TSMC manufacturing. We are excited to make AI easier and more affordable to deploy and use for all."

Tzvika Shmueli, Co-founder and VP of Operations at NeuReality